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ARCHITECTURAL
ALTERNATIVES
FOR
EXPLOITING
PARALLELISM

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Architectural Alternatives For Exploring Parallelism

Nicolas Lorente, Christian Joachim



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Architectural Alternatives for Exploiting Parallelism David J. Lilja, 1991 Graduates advanced undergraduates and practicing engineers of computer architecture or system design may find interest in the survey of various architectures within which a computer can be induced to walk and chew gum at the same time The 37 reprinted journal articles and conference presentation

Architecture and Design of Molecule Logic Gates and Atom Circuits Nicolas Lorente, Christian Joachim, 2013-02-01 Have you ever puzzled over how to perform Boolean logic at the atomic scale Or wondered how you can carry out more general calculations in one single molecule or using a surface dangling bond atomic scale circuit This volume gives you an update on the design of single molecule devices such as rectifiers switches and transistors more advanced semi classical and quantum boolean gates integrated in a single molecule or constructed atom by atom on a passivated semi conductor surface and describes their interconnections with adapted nano scale wiring The main contributors to the field of single molecule logic gates and surface dangling bond atomic scale circuits theory and design were brought together for the first time to contribute on topics such as molecule circuits surface dangling bond circuits quantum controlled logic gates and molecular qubits Contributions in this volume originate from the Barcelona workshop of the AtMol conference series held from January 12 13 2012

Memory Architecture Exploration for Programmable Embedded Systems Peter Grun, Nikil D. Dutt, Alexandru Nicolau, 2007-05-08 Memory Architecture Exploration for Programmable Embedded Systems addresses efficient exploration of alternative memory architectures assisted by a compiler in the loop that allows effective matching of the target application to the processor memory architecture This new approach for memory architecture exploration replaces the traditional black box view of the memory system and allows for aggressive co optimization of the programmable processor together with a customized memory system The book concludes with a set of experiments demonstrating the utility of this exploration approach The authors perform architecture and compiler exploration for a set of large real life benchmarks uncovering promising memory configurations from different perspectives such as cost performance and power

Power-Efficient Computer Architectures Magnus Själander, Margaret Martonosi, Stefanos Kaxiras, 2022-05-31 As Moore's Law and Dennard scaling trends have slowed the challenges of building high performance computer architectures while maintaining acceptable power efficiency levels have heightened Over the past ten years architecture techniques for power efficiency have shifted from primarily focusing on module level efficiencies toward more holistic design styles based on parallelism and heterogeneity This work highlights and synthesizes recent techniques and trends in power efficient computer architecture Table of Contents Introduction Voltage and Frequency Management Heterogeneity and Specialization Communication and Memory Systems Conclusions Bibliography Authors Biographies

Architecture Exploration for Embedded Processors with LISA Andreas Hoffmann, Heinrich Meyr, Rainer Leupers, 2013-06-29 Already today more than 90% of all programmable processors are employed in embedded systems This

number is actually not surprising contemplating that in a typical home you might find one or two PCs equipped with high of embedded systems performance standard processors but probably dozens including electronic entertainment household and telecom devices each of them equipped with one or more embedded processors Moreover the electronic components of upper class cars incorporate easily over one hundred processors Hence efficient embedded processor design is certainly an area worth looking at The question arises why programmable processors are so popular in embedded system design The answer lies in the fact that they help to narrow the gap between chip capacity and designer productivity Embedded processors cores are nothing but one step further towards improved design reuse just along the lines of standard cells in logic synthesis and macrocells in RTL synthesis in earlier times of IC design Additionally programmable processors permit to migrate functionality from hardware to software resulting in an even improved reuse factor as well as greatly increased flexibility

Journal of Research of the National Institute of Standards and Technology, 2001 Reports NIST research and development in the physical and engineering sciences in which the Institute is active These include physics chemistry engineering mathematics and computer sciences Emphasis on measurement methodology and the basic technology underlying standardization

Supercomputers in Seismic Exploration E. Eisner, 2013-10-22 Provides an authoritative overview of the role which computers now play in the field of seismology and discusses ways in which they can be improved for solving the increasingly complex problems now facing the scientist and engineer Topics covered include typical seismic models computational requirements associated with several standard numerical modelling techniques three dimensional processing migration and forward modelling advances in both hardware and software iterative modelling hypercube supercomputing reservoir simulation using supercomputers algorithms used in modelling and inversion wave equation computations and simulation of seismic waves

Navigating Computer Systems Architecture Barrett Williams, ChatGPT, 2024-10-23 Unlock the mysteries of computer systems architecture with Navigating Computer Systems Architecture an essential eBook for anyone eager to delve into the intricacies of computing This comprehensive guide offers a detailed roadmap through the dynamic landscape of computer architecture making complex concepts accessible and engaging Start your journey with a foundational understanding in Chapter 1 where the historical evolution of system architectures unfolds setting the stage for what's to come From there dive into the core components of computer organization uncovering the interplay between processor memory and I/O systems As you progress the essentials of digital logic and datapath design come to life complete with a practical case study on ALU design Explore the fundamental principles of Instruction Set Architecture ISA and gain a deep appreciation for its role in computing Discover the fascinating world of x86 ISA and RISC architecture analyzing their distinctive features and benefits Get equipped to understand pipeline architecture and the challenges of superscalar and VLIW designs laying the groundwork for mastering advanced performance technologies Memory management moves into the spotlight in subsequent chapters revealing the intricacies of

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Architecture of Computing Systems - ARCS 2007 Paul Lukowicz,2007-03-05 This book constitutes the refereed proceedings of the 20th International Conference on Architecture of Computing Systems ARCS 2007 held in Zurich Switzerland in March 2007 Coverage details a broad range of research topics related to basic technology architecture and application of computing systems with a strong focus on system aspects of pervasive computing and self organization techniques in both organic and autonomic computing

Parallel and Distributed Processing and Applications Jiannong Cao,2004-12-02 This book constitutes the refereed proceedings of the Second International Symposium on Parallel and Distributed Processing and Applications ISPA 2004 held in Hong Kong China in December 2004 The 78 revised full papers and 38 revised short papers presented were carefully reviewed and selected from 361 submissions The papers are organized in topical sections on parallel algorithms and systems data mining and management distributed algorithms and systems fault tolerance protocols and systems sensor networks and protocols cluster systems grid applications and systems peer to peer and ad hoc networking grid scheduling and algorithms data replication and caching software engineering and testing grid protocols context aware and mobile computing distributed routing and switching protocols cluster resource scheduling and algorithms security high performance processing networking and protocols artificial intelligence systems hardware architecture and implementations high performance computing architecture and distributed systems architecture

Parallel Computers 2 R.W Hockney,C.R Jesshope,1988-01-01 Since the publication of the first edition parallel computing technology has gained considerable momentum A large proportion of this has come from the improvement in VLSI techniques offering one to two orders of magnitude more devices than previously possible A second contributing factor in the fast development of the subject is commercialization The supercomputer is no longer restricted to a few well established research institutions and large companies A new computer breed combining the architectural advantages of the supercomputer with the advance of VLSI technology is now available at very attractive prices A pioneering device in this development is the transputer a VLSI processor specifically designed to operate in large concurrent systems *Parallel Computers 2 Architecture Programming and Algorithms* reflects the shift in emphasis of parallel computing and tracks the development of supercomputers in the years since the first edition was published It looks at large scale parallelism as found in transputer ensembles This extensively

rewritten second edition includes major new sections on the transputer and the OCCAM language The book contains specific information on the various types of machines available details of computer architecture and technologies and descriptions of programming languages and algorithms Aimed at an advanced undergraduate and postgraduate level this handbook is also useful for research workers machine designers and programmers concerned with parallel computers In addition it will serve as a guide for potential parallel computer users especially in disciplines where large amounts of computer time are regularly used

Handbook of Parallel Constraint Reasoning Youssef Hamadi,Lakhdar Sais,2018-04-05 This is the first book presenting a broad overview of parallelism in constraint based reasoning formalisms In recent years an increasing number of contributions have been made on scaling constraint reasoning thanks to parallel architectures The goal in this book is to overview these achievements in a concise way assuming the reader is familiar with the classical sequential background It presents work demonstrating the use of multiple resources from single machine multi core and GPU based computations to very large scale distributed execution platforms up to 80 000 processing units The contributions in the book cover the most important and recent contributions in parallel propositional satisfiability SAT maximum satisfiability MaxSAT quantified Boolean formulas QBF satisfiability modulo theory SMT theorem proving TP answer set programming ASP mixed integer linear programming MILP constraint programming CP stochastic local search SLS optimal path finding with A model checking for linear time temporal logic MC LTL binary decision diagrams BDD and model based diagnosis MBD The book is suitable for researchers graduate students advanced undergraduates and practitioners who wish to learn about the state of the art in parallel constraint reasoning

3D Modeling and Animation Nikos Sarris,Michael G. Strintzis,2005-01-01 3D Modeling and Animation Synthesis and Analysis Techniques for the Human Body covers the areas of modeling and animating 3D synthetic human models at a level that is useful to students researchers software developers and content generators The reader will be presented with the latest research level techniques for the analysis and synthesis of still and moving human bodies with particular emphasis in facial and gesture characteristics

Architectural Exaptation Alessandro Melis,Telmo Pievani,Jose Antonio Lara-Hernandez,2024-03-29 Architectural Exaptation When Function Follows Form focuses on the significance and the originality of the study of exaptation It presents exaptation as an opportunity to extend architectural design towards more sustainable approaches aimed at enforcing urban resilience The use of exaptation s definition in architecture supports the heuristic value of cross disciplinary studies on biology and architecture which seem even more relevant in times of global environmental crises This book aims to make a critique of the pre existing and extensive paternalistic literature Exaptation will be described as a functional shift of a structure that already had a prior but different function In architecture a functional shift of a structure that already had a function may apply to forms of decorative elements embedded in architectural components and to both change of function of tectonic elements and the change of use of an architectural space The book is illustrated with examples from around the globe including China Italy Mexico New

Zealand the USA and the UK and looks at different civilizations and diverse historical periods ranging from the urban to the architectural scale Such examples highlight the potential and latent human creative capacity to change the use and functions something that cities and buildings could consider when facing disturbances Exaptation is shown as an alternative narrative to the simplifications of evolutionary puritanism It also offers an innovative perspective and presents an opportunity to re think the manner in which we design and redesign our cities This book will be of interest to architecture planning urban design and biology researchers and students Winner of the Gerd Albers Award 2024 This award is for the best book contribution in the field of Urban Planning and is awarded by International Society of City and Regional Planners ISOCARP

Euro-Par '96 - Parallel Processing Luc Bouge,1996-08-14 Content Description Includes bibliographical references and index High Performance Data Mining Yike Guo,R.L. Grossman,1999 High Performance Data Mining Scaling Algorithms Applications and Systems brings together in one place important contributions and up to date research results in this fast moving area High Performance Data Mining Scaling Algorithms Applications and Systems serves as an excellent reference providing insight into some of the most challenging research issues in the field **Computer Architecture** John L. Hennessy,David A. Patterson,Krste Asanović,2012 The computing world is in the middle of a revolution mobile clients and cloud computing have emerged as the dominant paradigms driving programming and hardware innovation This book focuses on the shift exploring the ways in which software and technology in the cloud are accessed by cell phones tablets laptops and more **DSP Architecture Design Essentials** Dejan Marković,Robert W. Brodersen,2012-06-15 In DSP Architecture Design Essentials authors Dejan Markovi and Robert W Brodersen cover a key subject for the successful realization of DSP algorithms for communications multimedia and healthcare applications The book addresses the need for DSP architecture design that maps advanced DSP algorithms to hardware in the most power and area efficient way The key feature of this text is a design methodology based on a high level design model that leads to hardware implementation with minimum power and area The methodology includes algorithm level considerations such as automated word length reduction and intrinsic data properties that can be leveraged to reduce hardware complexity From a high level data flow graph model an architecture exploration methodology based on linear programming is used to create an array of architectural solutions tailored to the underlying hardware technology The book is supplemented with online material bibliography design examples CAD tutorials and custom software Algorithms for Memory Hierarchies Ulrich Meyer,Peter Sanders,2003-04-07 Algorithms that have to process large data sets have to take into account that the cost of memory access depends on where the data is stored Traditional algorithm design is based on the von Neumann model where accesses to memory have uniform cost Actual machines increasingly deviate from this model while waiting for memory access nowadays microprocessors can in principle execute 1000 additions of registers for hard disk access this factor can reach six orders of magnitude The 16 coherent chapters in this monograph like tutorial book introduce and survey algorithmic techniques used to achieve high performance

on memory hierarchies emphasis is placed on methods interesting from a theoretical as well as important from a practical point of view

Architecture of Topic Valéria Molnár, Verner Egerland, Susanne Winkler, 2019-08-19 This volume contains innovative papers that target the linguistic status of topic at the interface between grammar and discourse The purpose of the volume is to discuss the universal properties of topics and at the same time to document the range of discourse semantic and grammatical variation within this phenomenon in European languages The volume is structured accordingly i theoretical foundations of topicality in grammar and discourse ii discourse semantic correlates of topicality iii variation in the grammatical external and internal encoding of topicality iv topics from the diachronic perspective The articles take different perspectives including contrastive studies of modern languages studies on diachronic development and typological generalizations They also take into consideration various types of empirical data introspective data semi spontaneously produced data experimental data and language corpora The articles in this volume show that the concept of topic is necessary for the description and explanation of a number of discourse semantic phenomena They present a state of the art account of the architecture of topic while making recent research on the phenomenon accessible to a wider readership

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