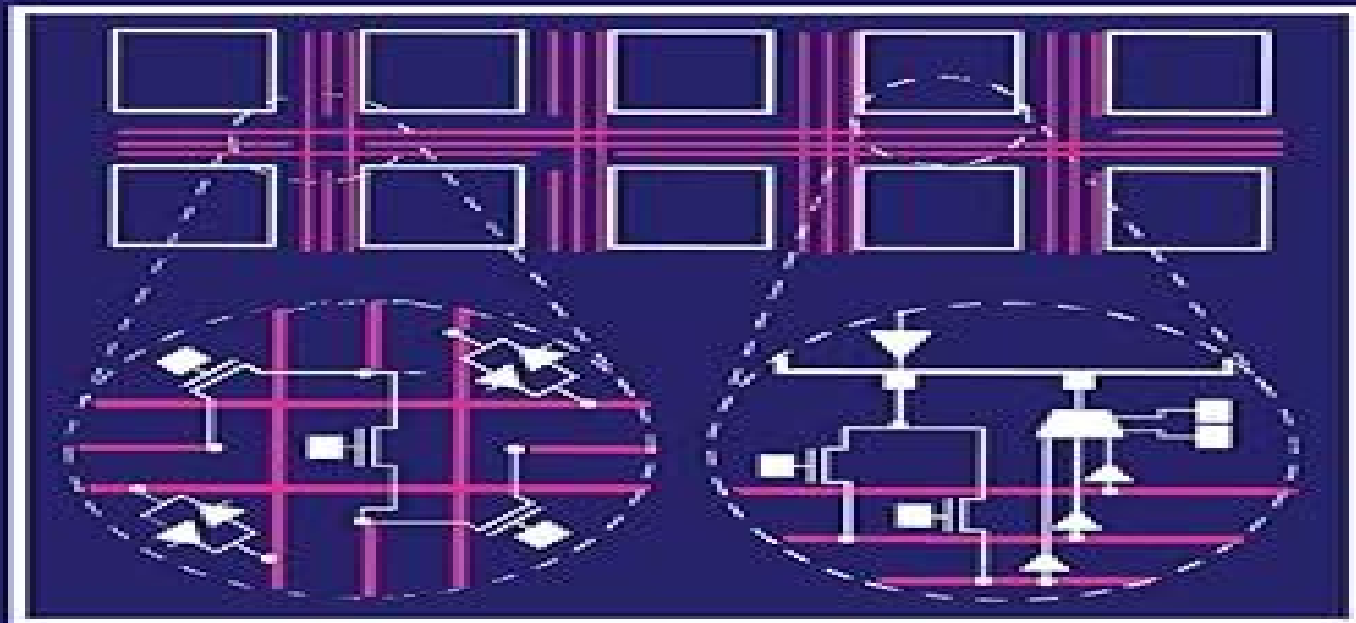


Architecture and CAD for Deep-Submicron FPGAs



Vaughn Betz
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Architecture And Cad For Deep Submicron Fpgas

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Architecture and CAD for Deep-Submicron FPGAS Vaughn Betz, Jonathan Rose, Alexander Marquardt, 2012-12-06 Since their introduction in 1984 Field Programmable Gate Arrays FPGAs have become one of the most popular implementation media for digital circuits and have grown into a 2 billion per year industry As process geometries have shrunk into the deep submicron region the logic capacity of FPGAs has greatly increased making FPGAs a viable implementation alternative for larger and larger designs To make the best use of these new deep submicron processes one must re design one s FPGAs and Computer Aided Design CAD tools Architecture and CAD for Deep Submicron FPGAs addresses several key issues in the design of high performance FPGA architectures and CAD tools with particular emphasis on issues that are important for FPGAs implemented in deep submicron processes Three factors combine to determine the performance of an FPGA the quality of the CAD tools used to map circuits into the FPGA the quality of the FPGA architecture and the electrical i e transistor level design of the FPGA Architecture and CAD for Deep Submicron FPGAs examines all three of these issues in concert In order to investigate the quality of different FPGA architectures one needs CAD tools capable of automatically implementing circuits in each FPGA architecture of interest Once a circuit has been implemented in an FPGA architecture one next needs accurate area and delay models to evaluate the quality speed achieved area required of the circuit implementation in the FPGA architecture under test This book therefore has three major foci the development of a high quality and highly flexible CAD infrastructure the creation of accurate area and delay models for FPGAs and the study of several important FPGA architectural issues Architecture and CAD for Deep Submicron FPGAs is an essential reference for researchers professionals and students interested in FPGAs

Disruptive Logic Architectures and Technologies

Pierre-Emmanuel Gaillardon, Ian O'Connor, Fabien Clermidy, 2012-04-24 This book discusses the opportunities offered by disruptive technologies to overcome the economical and physical limits currently faced by the electronics industry It provides a new methodology for the fast evaluation of an emerging technology from an architectural prospective and discusses the implications from simple circuits to complex architectures Several technologies are discussed ranging from 3 D integration of devices Phase Change Memories Monolithic 3 D Vertical NanoWires based transistors to dense 2 D arrangements Double Gate Carbon Nanotubes Sublithographic Nanowires Lithographic Crossbar arrangements Novel architectural organizations as well as the associated tools are presented in order to explore this freshly opened design space

Principles and Structures of FPGAs

Hideharu Amano, 2018-09-03 This comprehensive textbook on the field programmable gate array FPGA covers its history fundamental knowledge architectures device technologies computer aided design technologies design tools examples of application and future trends Programmable logic devices represented by FPGAs have been rapidly developed in recent years and have become key electronic devices used in most IT products This book provides both complete introductions suitable for students and beginners and high level techniques useful for engineers and researchers in

this field Differently developed from usual integrated circuits the FPGA has unique structures design methodologies and application techniques Allowing programming by users the device can dramatically reduce the rising cost of development in advanced semiconductor chips The FPGA is now driving the most advanced semiconductor processes and is an all in one platform combining memory CPUs and various peripheral interfaces This book introduces the FPGA from various aspects for readers of different levels Novice learners can acquire a fundamental knowledge of the FPGA including its history from Chapter 1 the first half of Chapter 2 and Chapter 4 Professionals who are already familiar with the device will gain a deeper understanding of the structures and design methodologies from Chapters 3 and 5 Chapters 6 8 also provide advanced techniques and cutting edge applications and trends useful for professionals Although the first parts are mainly suitable for students the advanced sections of the book will be valuable for professionals in acquiring an in depth understanding of the FPGA to maximize the performance of the device Field-Programmable Logic and Applications Gordon

Brebner,2001-08-15 This book constitutes the refereed proceedings of the 11th International Conference on Field Programmable Logic and Application FPL 2001 held in Belfast Northern Ireland UK in August 2001 The 56 revised full papers and 15 short papers presented were carefully reviewed and selected from a total of 117 submissions The book offers topical sections on architectural framework place and route architecture DSP synthesis encryption runtime reconfiguration graphics and vision networking processor interaction applications methodology loops and systolic image processing faults and arithmetic *Modern Programmable Interconnect Design* Stefan Nikolić,2025-03-06 This book thoroughly analyzes the fundamental problem of programmable interconnect design in FPGAs Recent changes in fabrication technology have caused a renewed interest in this problem that was formerly considered largely solved Besides presenting some of the most recent research results directed towards resolving the difficulties of programmable interconnect design in scaled technologies the author also gives a modern introduction to programmable interconnect design in general covering related topics such as broader FPGA architecture CAD and Networks on Chip enabling practitioners from other fields to understand the significance and implications of this important problem **Tree-based Heterogeneous FPGA Architectures** Umer

Farooq,Zied Marrakchi,Habib Mehrez,2012-05-17 This book presents a new FPGA architecture known as tree based FPGA architecture due to its hierarchical nature This type of architecture has been relatively unexplored despite their better performance and predictable routing behavior as compared to mesh based FPGA architectures In this book we explore and optimize the tree based architecture and we evaluate it by comparing it to equivalent mesh based FPGA architectures

Embedded Systems Handbook Richard Zurawski,2018-09-03 Considered a standard industry resource the Embedded Systems Handbook provided researchers and technicians with the authoritative information needed to launch a wealth of diverse applications including those in automotive electronics industrial automated systems and building automation and control Now a new resource is required to report on current developments and provide a technical reference for those

looking to move the field forward yet again Divided into two volumes to accommodate this growth the Embedded Systems Handbook Second Edition presents a comprehensive view on this area of computer engineering with a currently appropriate emphasis on developments in networking and applications Those experts directly involved in the creation and evolution of the ideas and technologies presented offer tutorials research surveys and technology overviews that explore cutting edge developments and deployments and identify potential trends This first self contained volume of the handbook Embedded Systems Design and Verification is divided into three sections It begins with a brief introduction to embedded systems design and verification It then provides a comprehensive overview of embedded processors and various aspects of system on chip and FPGA as well as solutions to design challenges The final section explores power aware embedded computing design issues specific to secure embedded systems and web services for embedded devices Those interested in taking their work with embedded systems to the network level should complete their study with the second volume Network Embedded Systems

Embedded Systems Handbook 2-Volume Set Richard Zurawski, 2018-10-08 During the past few years there has been an dramatic upsurge in research and development implementations of new technologies and deployments of actual solutions and technologies in the diverse application areas of embedded systems These areas include automotive electronics industrial automated systems and building automation and control Comprising 48 chapters and the contributions of 74 leading experts from industry and academia the Embedded Systems Handbook Second Edition presents a comprehensive view of embedded systems their design verification networking and applications The contributors directly involved in the creation and evolution of the ideas and technologies presented offer tutorials research surveys and technology overviews exploring new developments deployments and trends To accommodate the tremendous growth in the field the handbook is now divided into two volumes New in This Edition Processors for embedded systems Processor centric architecture description languages Networked embedded systems in the automotive and industrial automation fields Wireless embedded systems Embedded Systems Design and Verification Volume I of the handbook is divided into three sections It begins with a brief introduction to embedded systems design and verification The book then provides a comprehensive overview of embedded processors and various aspects of system on chip and FPGA as well as solutions to design challenges The final section explores power aware embedded computing design issues specific to secure embedded systems and web services for embedded devices Networked Embedded Systems Volume II focuses on selected application areas of networked embedded systems It covers automotive field industrial automation building automation and wireless sensor networks This volume highlights implementations in fast evolving areas which have not received proper coverage in other publications Reflecting the unique functional requirements of different application areas the contributors discuss inter node communication aspects in the context of specific applications of networked embedded systems

Energy-Aware Systems and Networking for Sustainable Initiatives Kaabouch, Naima, Hu, Wen-Chen, 2012-06-30 This book covers a great variety of topics such as

materials environment electronics and computing offering a vital source of information detailing the latest architectures frameworks methodologies and research on energy aware systems and networking for sustainable initiatives

Nanotechnology Rainer Waser, 2008-07-21 This second of two volumes on applications in information technology is divided into two main sections The first covers logic devices and concepts ranging from advanced and non conventional CMOS and semiconductor nanowire devices via various spin controlled logic devices and concepts involving carbon nanotubes organic thin films as well as single organic molecules right up to the visionary idea of intramolecular computation The second part architectures and computational concepts discusses biologically inspired structures and quantum cellular automata finishing off by summarizing the main principles and current approaches to coherent solid state based quantum computation

Design of Interconnection Networks for Programmable Logic Guy Lemieux, David Lewis, 2013-06-29 Programmable Logic Devices PLDs have become the key implementation medium for the vast majority of digital circuits designed today While the highest volume devices are still built with full fabrication rather than field programmability the trend towards ever fewer ASICs and more FPGAs is clear This makes the field of PLD architecture ever more important as there is stronger demand for faster smaller cheaper and lower power programmable logic PLDs are 90% routing and 10% logic This book focuses on that 90% that is the programmable routing the manner in which the programmable wires are connected and the circuit design of the programmable switches themselves Anyone seeking to understand the design of an FPGA needs to become literate in the complexities of programmable routing architecture This book builds on the state of the art of programmable interconnect by providing new methods of investigating and measuring interconnect structures as well as new programmable switch basic circuits The early portion of this book provides an excellent survey of interconnect structures and circuits as they exist today Lemieux and Lewis then provide a new way to design sparse crossbars as they are used in PLDs and show that the method works with an empirical validation This is one of a few routing architecture works that employ analytical methods to deal with the routing architecture design The analysis permits interesting insights not typically possible with the standard empirical approach

Quantifying and Exploring the Gap Between FPGAs and ASICs Ian Kuon, Jonathan Rose, 2010-07-03 Field programmable gate arrays FPGAs which are pre fabricated programmable digital integrated circuits ICs provide easy access to state of the art integrated circuit process technology and in doing so democratize this technology of our time This book is about comparing the qualities of FPGA their speed performance area and power consumption against custom fabricated ICs and exploring ways of mitigating their deficiencies This work began as a question that many have asked and few had the resources to answer how much worse is an FPGA compared to a custom designed chip As we dealt with that question we found that it was far more difficult to answer than we anticipated but that the results were rich basic insights on fundamental understandings of FPGA architecture It also encouraged us to find ways to leverage those insights to seek ways to make FPGA technology better which is what the second half of the book is about

While the question How much worse is an FPGA than an ASIC has been a constant sub theme of all research on FPGAs it was posed most directly some time around May 2004 by Professor Abbas El Gamal from Stanford University to us he was working on a 3D FPGA and was wondering if any real measurements had been made in this kind of comparison Shortly thereafter we took it up and tried to answer in a serious way

Nanoelectronic Circuit Design Niraj K. Jha, Deming Chen, 2010-12-21 This book is about large scale electronic circuits design driven by nanotechnology where nanotechnology is broadly defined as building circuits using nanoscale devices that are either implemented with nanomaterials e g nanotubes or nanowires or following an unconventional method e g FinFET or III V compound based devices These nanoscale devices have significant potential to revolutionize the fabrication and integration of electronic systems and scale beyond the perceived scaling limitations of traditional CMOS While innovations in nanotechnology originate at the individual device level realizing the true impact of electronic systems demands that these device level capabilities be translated into system level benefits This is the first book to focus on nanoscale circuits and their design issues bridging the existing gap between nanodevice research and nanosystem design

Language-driven Exploration and Implementation of Partially Re-configurable ASIPs Anupam Chattopadhyay, Rainer Leupers, Heinrich Meyr, Gerd Ascheid, 2008-12-02 Increasing complexity of modern embedded systems demands system designers to ramp up their design productivity without compromising performance goals This is promoted by modern Electronic System Level ESL techniques Language driven Exploration and Implementation of Partially Reconfigurable ASIPs addresses an important segment of the ESL area by modeling partially reconfigurable processors via high level Architecture Description Language ADL This approach also hints an imminent evolution in the area of reconfigurable system design

Reconfigurable Computing Scott Hauck, André DeHon, 2010-07-26 Reconfigurable Computing marks a revolutionary and hot topic that bridges the gap between the separate worlds of hardware and software design the key feature of reconfigurable computing is its groundbreaking ability to perform computations in hardware to increase performance while retaining the flexibility of a software solution Reconfigurable computers serve as affordable fast and accurate tools for developing designs ranging from single chip architectures to multi chip and embedded systems Scott Hauck and Andre DeHon have assembled a group of the key experts in the fields of both hardware and software computing to provide an introduction to the entire range of issues relating to reconfigurable computing FPGAs field programmable gate arrays act as the computing vehicles to implement this powerful technology Readers will be guided into adopting a completely new way of handling existing design concerns and be able to make use of the vast opportunities possible with reconfigurable logic in this rapidly evolving field Designed for both hardware and software programmers Views of reconfigurable programming beyond standard programming languages Broad set of case studies demonstrating how to use FPGAs in novel and efficient ways

System-on-Chip Bashir M. Al-Hashimi, 2006-01-31 This book highlights both the key achievements of electronic systems design targeting SoC implementation style and the future challenges presented by the

continuing scaling of CMOS technology

EDA for IC Implementation, Circuit Design, and Process Technology

Luciano Lavagno, Louis Scheffer, Grant Martin, 2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The second volume EDA for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic to GDSII a file format used to transfer data of semiconductor physical layout analog mixed signal design physical verification and technology CAD TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability at the nanoscale power supply network design and analysis design modeling and much more Save on the complete set

Energy Efficient Hardware-Software Co-Synthesis Using Reconfigurable Hardware

Jingzhao Ou, Viktor K. Prasanna, 2009-10-14 Rapid energy estimation for energy efficient applications using field programmable gate arrays FPGAs remains a challenging research topic Energy dissipation and efficiency have prevented the widespread use of FPGA devices in embedded systems Helping overcome these challenges this book offers solutions for the development of energy efficient applications using FPGAs It provides a framework for high level hardware software application development describes energy performance modeling for reconfigurable system on chip devices and explores energy efficient designs for various applications The authors present a two step rapid energy estimation technique that enables high level design space exploration and offer a hardware software design for energy efficient implementations of operating systems

System-on-Chip for Real-Time Applications

Wael Badawy, Graham A. Julien, 2012-12-06 System on Chip for Real Time Applications will be of interest to engineers both in industry and academia working in the area of SoC VLSI design and application It will also be useful to graduate and undergraduate students in electrical and computer engineering and computer science A selected set of papers from the 2nd International Workshop on Real Time Applications were used to form the basis of this book It is organized into the following chapters Introduction Design Reuse Modeling Architecture Design Techniques Memory Circuits Low Power Interconnect and Technology MEMS System on Chip for Real Time Applications contains many signal processing applications and will be of particular interest to those working in that community

Field Programmable Logic and Application

Jürgen Becker, Marco Platzner, Serge Vernalde, 2004-08-11 This book contains the papers presented at the 14th International Conference on Field Programmable Logic and Applications FPL held during August 30th September 1st 2004 The conference was hosted by the Interuniversity Micro Electronics Center IMEC in Leuven Belgium The FPL series of conferences was founded in 1991 at Oxford University UK and has been held annually since in Oxford 3 times Vienna Prague Darmstadt London Tallinn Glasgow Villach Belfast Montpellier and Lisbon It is the largest and oldest conference in reconfigurable computing and brings together academic researchers industry experts users and newcomers in an informal welcoming atmosphere that encourages productive exchange of ideas and knowledge between the delegates The fast and exciting advances in field programmable logic are

increasing steadily with more and more application potential and need New ground has been broken in architectures design techniques partial run time reconfiguration and applications of field programmable devices in several different areas Many of these recent innovations are reported in this volume The size of the FPL conferences has grown significantly over the years FPL in 2003 saw 216 papers submitted The interest and support for FPL in the programmable logic community continued this year with 285 scientific papers submitted demonstrating a 32% increase when compared to the year before The technical program was assembled from 78 selected regular papers 45 additional short papers and 29 posters resulting in this volume of proceedings The program also included three invited plenary keynote presentations from Xilinx Gilder Technology Report and Altera and three embedded tutorials from Xilinx the Universit at Karlsruhe TH and the University of Oslo

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